

ABSTRACT OF THE DISCLOSURE

A plurality of switch connectors are arranged in each of a plurality of memory slots. Each of the plurality of switch connectors includes a first-type connector pin and a second-type connector pin. The first-type connector pin is coupled to one of a plurality 5 of signal lines of a memory bus. The second-type connector pin is coupled to impedance matching circuits included in an load circuit. In each of the memory slots in which the memory module is not inserted, a pair of the first-type connector pin and the second-type connector pin provide flexible electric contact. In each of the memory slots in which the memory module is inserted, the first-type connector pin and the second-type 10 connector pin are physically separated and insulated from each other. The first-type connector pin provides electric contact with module pins of the memory module.